

T-2.5G-GPON-OLT-20KM-C

OLT for ITU-T G.984.2 Class C+

2.488Gbps Downstream and 1.244Gbps Upstream

Features

Single Fiber Transceiver with single mode SC receptacle
1490nm continuous-mode transmitter with DFB laser
1310nm burst-mode receiver with APD-TIA
Single 3.3V power supply
Reset burst-mode receiver design
Burst mode signal detect output which is LVTTTL compatible.
Burst mode received signal strength indication (RSSI) output
Complies with ITU-T G984.2 Class C+
Digital diagnostic interface compliant with SFF-8472 Rev 9.4 ,
Complies with RoHS directive (2002/95/EC)
Operating case temperature: Standard: 0 to +70°C



Applications

Gigabit Passive Optical Networks (G-PON) – OLT side

Ordering information

Model No.	Product Description
T-2.5G-GPON-OLT-20KM-C	Tx1490nm/Tx1310nm, 2.5Gbps/1.25Gbps, SC, 20km, Class C+, 0°C~+70°C, With DDM

Description

The T-2.5G-GPON-OLT-20KM-C transceiver is the high performance module for single fiber by using 1490nm continuous-mode transmitter and 1310nm burst-mode receiver. It is optical line terminal(OLT) for ITU-T G984.2. The optical transceiver is compliant with the Small Form- Factor Pluggable (SFP) Multi-Source Agreement (MSA).

The transmitter section uses a 1490nm DFB LD with automatic power control (APC) function and temperature compensation circuitry to ensure stable extinction ratio over all operating temperature range. and is Class I laser compliant IEC825 and CDRH standards. The receiver has a hermetically packaged burst-mode APD-TIA (trans-impedance amplifier) pre-amplifier and a burst-mode limiting amplifier with LVPECL compatible differential outputs.

The receiver also includes the function of burst mode signal detect output and fast RSSI output which is enabled by atrigger. When the burst optical power is on, the receiver outputs high level; when the burst optical power is off, the receiver outputs low level. Fast RSSI function can satisfy more severe timing demand to monitor the power from any ONU.

Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Notes
Storage Temperature	Tst	-40	+85	°C	-
Operating Case Temperature	Tc	0	70	°C	-
Operating Humidity	RH	5	90	%	Non-condensing
Input Voltage	-	GND	Vcc	V	-
Power Supply Voltage	Vcc-Vee	0	3.6	V	-

Recommended Operating Conditions

Table 2 - Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard Tc	0	-	+70	°C
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Power Supply Current	Icc	-	-	500	mA

Optical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Data Rate			2.5		Gb/S	
Centre Wavelength	λ_c	1480		1500	nm	
Spectral Width	$\Delta\lambda$		0.4	1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Average Output Power	Pout	3		7	dBm	1
Extinction Ratio	ER	8.2			dB	
Average Launch Power-OFF Transmitter	Poff			-40	dBm	
Optical Eye Diagram	Compliant with G984.2					
Optical Rise/Fall Time (20%~80%)	tr/tf			160	ps	
Data Input Swing Differential	V _{IN}	200		2400	mV	2
Input Differential Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0	Vcc	V	
	Enable		0	0.8	V	
TX Fault	Fault		2.0	Vcc	V	
	Normal		0	0.8	V	

Receiver						
Data Rate			1.25		Gb/S	
Centre Wavelength	λ_c	1260		1360	nm	
Receiver Sensitivity	Sen			-30	dBm	3
Receiver Overload	Sat	-12			dBm	3
Receiver CID Tolerance		72			Bit	
Receiver Reflectance				-20	dB	
Data Output Voltage - High	VOH	VccR -1.05		VccR - 0.85	V	4
Data Output Voltage - Low	VOL	VccR -1.84		VccR - 1.60	V	4
Burst Detect De-assert	BSD_D	-45			dBm	
Burst Detect Assert	BSD_A			-32	dBm	
Burst Detect Hysteresis		1		6	dBm	
BRST_Det High	V_DET_H	2.4		VCC	V	
BRST_Det Low	V_DETL	0		0.4	V	
Receiver Power DDM (RSSI) Error	RXDDM			+/-3	dBm	5

Notes:

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 223-1 test pattern @1250Mbps, BER $\leq 1 \times 10^{-10}$.
4. Internally DC-coupled.
5. RSSI DDM working range is between -12 to -31 dBm. RSSI DDM accuracy is better than +/- 3dB for input power levels between -12 to -31 dBm, the accuracy reduces to +/- 5 dBm for other input power levels.

Diagnostics

Table 5 – Diagnostics Specification

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	$\pm 3^\circ\text{C}$	Internal / External
Voltage	3.0 to 3.6	V	$\pm 3\%$	Internal / External
Bias Current	0 to 100	mA	$\pm 10\%$	Internal / External
TX Power	3 to 7	dBm	$\pm 3\text{dB}$	Internal / External
RX Power	-31 to -12	dBm	$\pm 3\text{dB}$	Internal / External

Timing Characteristics for Digital RSSI

Parameter	Symbol	Min.	TYP	MAX	UNITS
Trigger delay	Td	30			ns
Sample time	Ts	300			ns
Internal I2C Delay	TI2C			500	us
Digital RSSI		Figure 1			

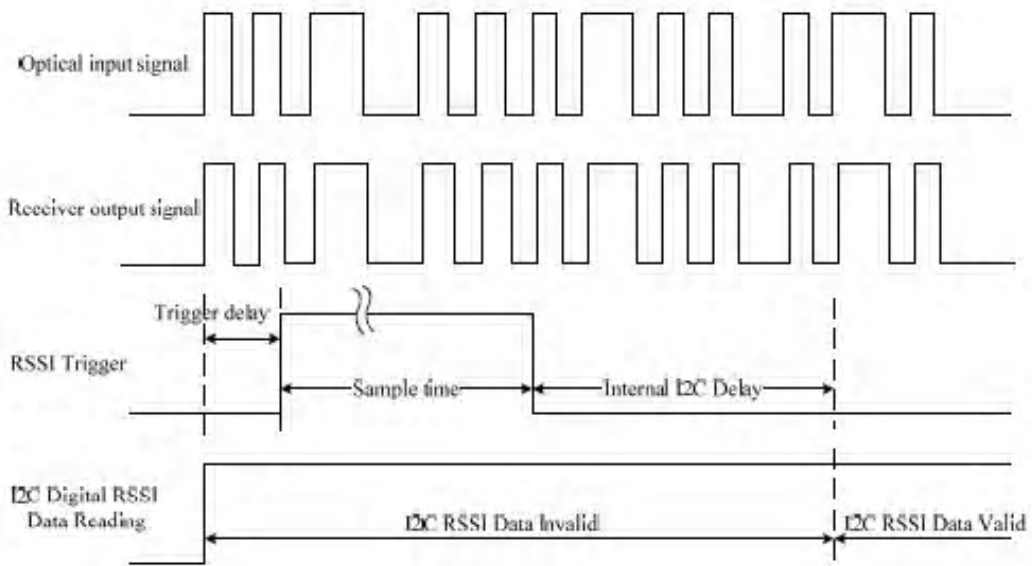


Figure 1 Digital RSSI Timing

Timing Characteristics for Reset

Parameter	Symbol	Min.	TYP	MAX	UNITS
RESET Input Signal (RESET+)	Reset	LVTTL			
RESET Signal Width	Tr		24		Bits
Guard time	Tg	32			Bits
Preamble time	Tp	44			Bits
Reset Delay	Δt			Tg - Tr	Bits
Burst Signal Detect On	T_BSD_On			20	ns
Burst Signal Detect Off	T_BSD_Off			20	ns
IRESET Timing		Figure 2			

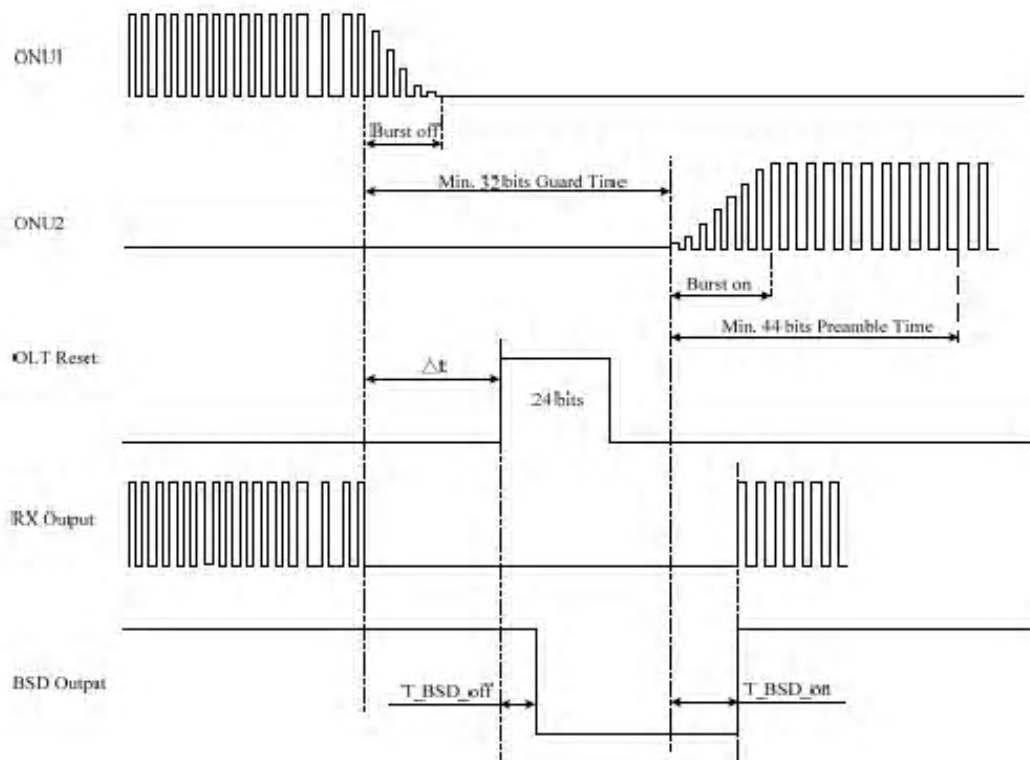


Figure 2 Reset Timing

Digital Diagnostic Memory Map

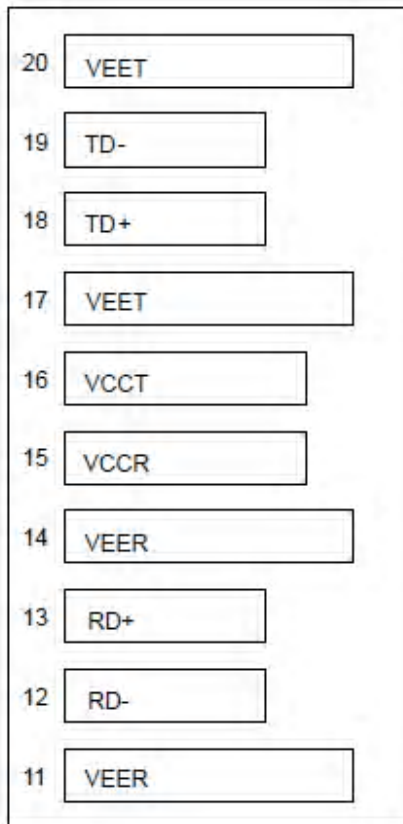
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

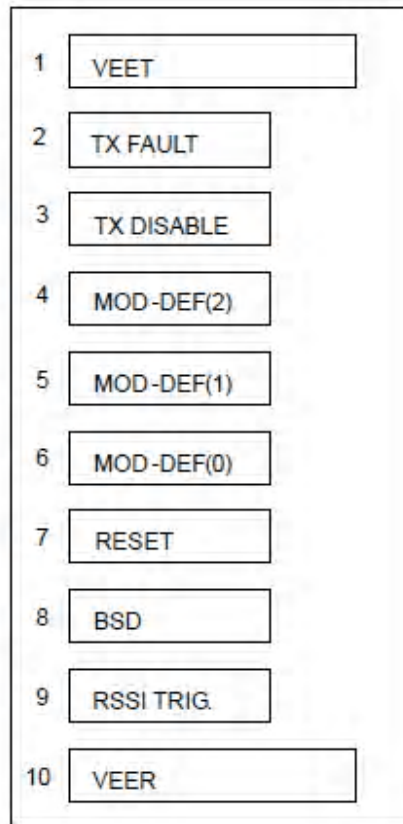
The digital diagnostic memory map specific data field defines as following.

2 wire address 1010000X (A0h)	2 wire address 1010001X (A2h)
0	0
Serial ID Defined by SFP MSA (96 bytes)	Alarm and Warning Thresholds (56 bytes)
95	55
Vendor Specific (32 bytes)	Cal Constants (40 bytes)
127	95
Reserved in SFP MSA (128 bytes)	Real Time Diagnostic Interface (24 bytes)
	119
	127
	Vendor Specific (8 bytes)
	User Writable EEPROM (120 bytes)
	247
	255
255	Vendor Specific (8 bytes)
	255

Pin Diagram



Top of Board



Bottom of Board

Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	RESET	LVTTL input. Assert "Reset" high at the end of previous burst, 16 bits in	3	Note 4
8	BSD	Burst signal detect	3	Note 5
9	RSSI TRIG.	CMOS input. Assert high at the beginning of the monitored burst	3	Note 6
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 7
13	RD+	Received Data Out	3	Note 7
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	V _{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and V_{cc}+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.

2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

Low (0 to 0.8V): Transmitter on

(>0.8V, < 2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be V_{ccT} or V_{ccR}. Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID

4) RESET is a LVTTL input. When the previous burst signal package is end, the host will give a "high" RESET to restore the state of LA. Internal pull-down 10K resistor to GND.

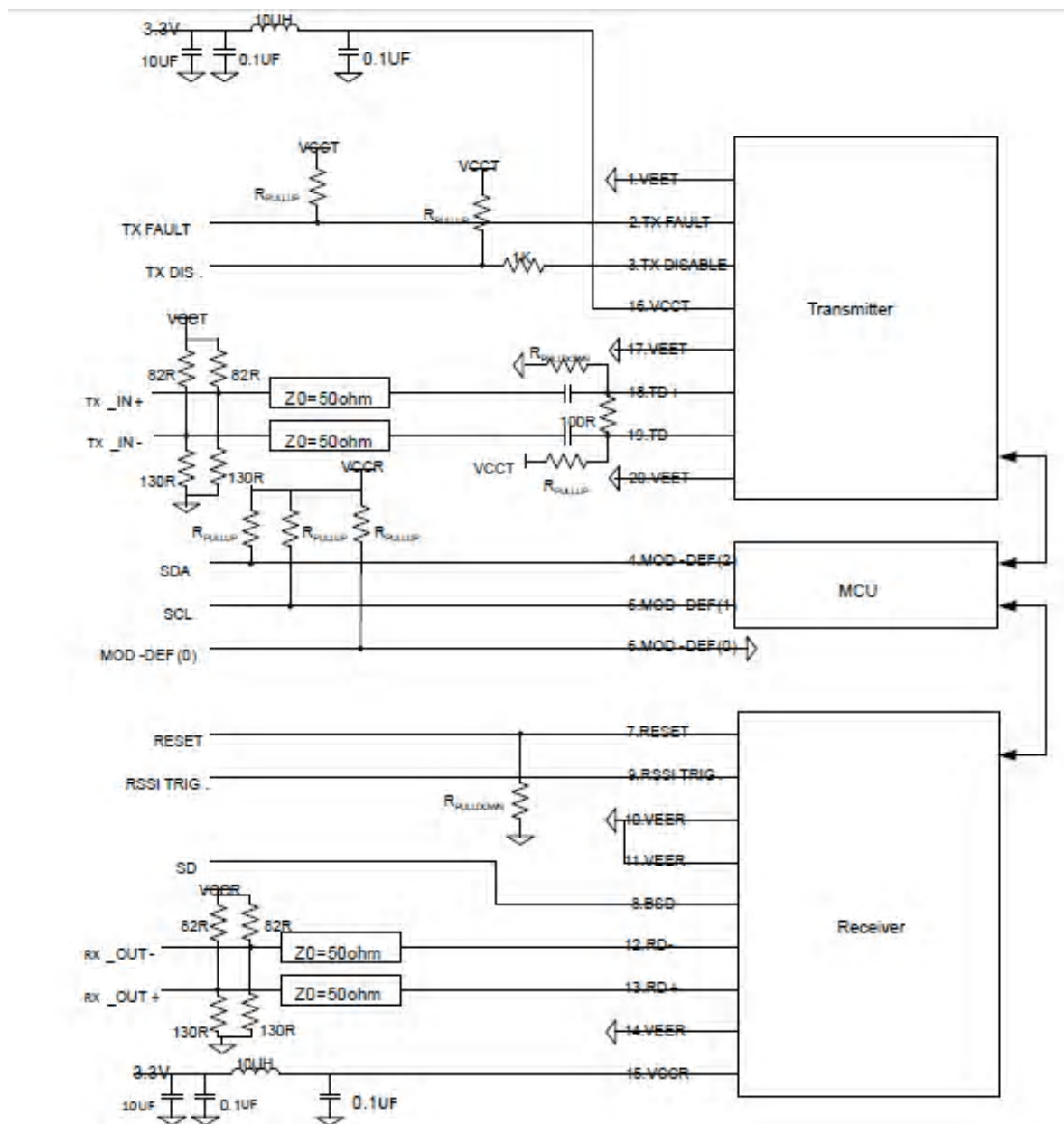
5) BSD can track the state of receiving burst signal. Logic 0 indicates loss of signal; Logic1 indicates receiving signal packages.

6) RSSI TRIG is a CMOS input. Assert high after 30ns delay time of the beginning of the monitored burst package, at least 300ns in duration.

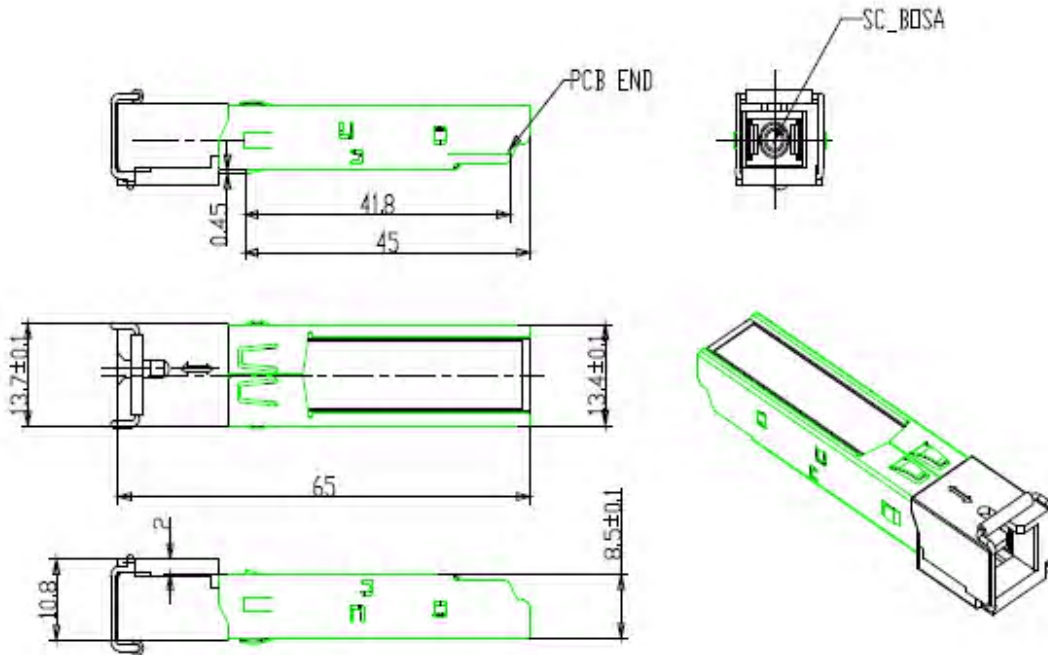
7) RD-/+ : These are the differential receiver outputs. They are internally DC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.

8) TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



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